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Tutorial for Program Verification Exercise Sheet 14

In this exercise sheet we work with the Hoare proof system, control flow graphs, and reachability graphs.

Submit your solution by uploading it as PDF in ILIAS.

Exercise 1: Alternative Assume Axiom

In the lecture, we introduced the following axiom for the **assume** statement in the Hoare proof system:

$$\{arphi\} ext{ assume expr; } \{arphi \wedge ext{ expr}\} \ ^{(assu)}$$

Alternatively, we could have introduced the following axiom:

$$\overline{\{\texttt{expr}
ightarrow\psi\}}\ \texttt{assume expr;}\ \{\psi\}\ (assu')$$

In this exercise we will show that both rules are equivalent.

- (a) Give a proof for the Hoare triple $\{expr \rightarrow \psi\}$ assume expr; $\{\psi\}$ (for an arbitrary formula ψ) using the Hoare proof system.
- (b) Give a proof of the Hoare triple $\{\varphi\}$ assume expr; $\{\varphi \land expr\}$ for an arbitrary formula φ . Use a modified variant of the Hoare proof system, where the rule (*assu*) has been replaced by the rule (*assu*').

Exercise 2: From Programs to CFGs

2 Points

For each of the programs given below, draw a control-flow graph.

(a) Code of program P_{pow} :

1 e := 1; 2 z := 0; 3 while (z < y) { 4 e := e * x; 5 z := z + 1; 6 } 2 Points

(b) Code of program P_{findmin} :

1 i := lo; 2 min := a[lo, lo]; 3 while (i <= hi) { j := lo; 4 while (j <= hi) {</pre> if (a[i, j] < min) {</pre> 6 min := a[i, j]; } j := j + 1; 9 } i := i + 1; 11 12 **}**

Exercise 3: Program Configurations

2 Points

Consider the program $P = (V, \mu, \mathcal{T})$ with $V = \{x, y\}, \mu(x) = \mu(y) = \{\text{true}, \text{false}\}$ and \mathcal{T} a derivation tree for the statement below on the left. On the right, a CFG for P is shown.



Draw the reachability graph for this control-flow graph and the precondition-postconditionpair $(x, x \to \neg y)$.

Exercise 4: Existence of Program Executions 2 Points

Prove the following lemma from the lecture slides.

Lemma (RelAndExec.2) Let $G = (Loc, \Delta, \ell_{init}, \ell_{ex})$ be a control-flow graph for the sequential composition st_1st_2 . There exists a program execution $(\ell_0, s_0), \ldots, (\ell_n, s_n)$ with $\ell_0 = \ell_{\text{init}}$ and $\ell_n = \ell_{\text{ex}}$, iff $(s_0, s_n) \in [st_1st_2]$.